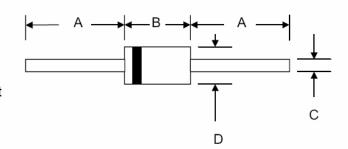
# RL201-RL207 2.0 SILICON RECTIFIER

Technical Data Data Sheet N0559, Rev. - **Green Products** 

#### **Features**

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- This is a Pb Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request



#### **Mechanical Data**

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.40 grams (approx.)
- Mounting Position: Any
- Marking: Part Name, SSG and Date Code

DO-15									
Dim	Min	Max	Min	Max					
Α	25.4	_	1.000	_					
В	5.50	7.62	0.217	0.300					
С	0.71	0.864	0.028	0.034					
D	2.60	3.60	0.102	0.142					
	In mm		In inch						

## **Marking Diagram:**

Where XXXXX is YYWWL



RL201 = Part Name
SSG = SSG
YY = Year
WW = Week
L = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

# Ordering Information

Device	Package	Shipping
RL201-RL207	DO-15 (Pb-Free)	3000pcs / tape

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification.

<sup>•</sup> Weiqi Street, Airport Development Zone, Jiangning District, Nanjing, China 211113 🗏 (86) 25-87123907 •

<sup>•</sup> FAX (86) 25-87123900 • World Wide Web Site - http://www.sangdest.com.cn • E-Mail Address - sales@ sangdest.com.cn •





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### Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

			•						
Characteristic	Symbol	RL201	RL202	RL203	RL204	RL205	RL206	RL207	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	50	100	200	400	600	800	1000	٧
RMS Reverse Voltage	VR(RMS)	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @T <sub>A</sub> = 75°C	lo	2.0					Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	70					Α		
Forward Voltage @I <sub>F</sub> = 2.0A	VFM	1.0					V		
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	lгм	5.0 50					μΑ		
Typical Junction Capacitance (Note 2)	Cj	20					pF		
Typical Thermal Resistance Junction to Ambient (Note 1)	$R_{\theta}$ JA	40				K/W			
Operating Temperature Range	Tj	-65 to +125				°C			
Storage Temperature Range	Тѕтс	-65 to +150				°C			

#### \*Glass passivated forms are available upon request

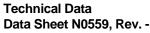
Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Measured at 1.0 MHz and Applied Reverse Voltage of 4.0V D.C.

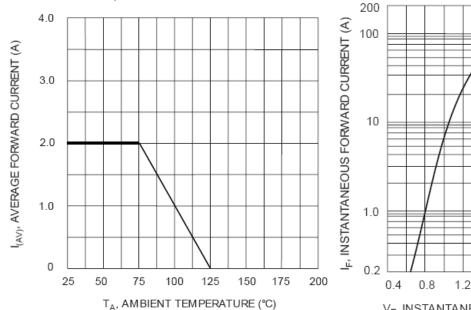
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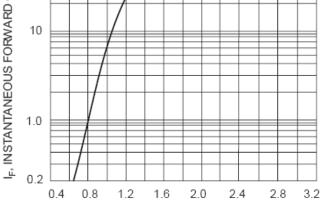
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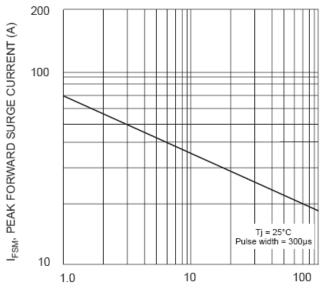
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T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve

V<sub>F</sub>, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics





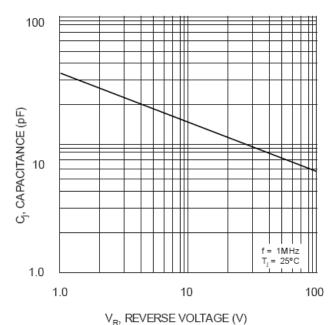


Fig. 4 Typical Junction Capacitance

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