

GY Dual Micropower Zero-Drift Operational Amplifier with Internal Capacitors

### DESCRIPTION

The LTC<sup>®</sup>1047 is a micropower, high performance dual zero-drift operational amplifier. The sample-and-hold capacitors usually required by other chopper amplifiers are integrated on-chip, minimizing the need for external components.

The LTC1047 has a typical offset voltage of  $3\mu$ V, drift of 10nV/°C, input noise voltage typically  $3.5\mu$ V<sub>P-P</sub>, and typical voltage gain of 150dB. The common mode rejection is 110dB minimum, with minimum power supply rejection of 105dB. The LTC1047 also offers a 0.2V/µs slew rate and a gain bandwidth product of 200kHz. Overload recovery time from saturation is 70ms, four times faster than chopper amplifiers with external capacitors.

The LTC1047 is available in a standard plastic 8-pin PDIP as well as a 16-pin SW package. The LTC1047 is a plug-in replacement for most dual op amps with improved DC performance and substantial power savings.

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### FEATURES

- No External Components Required
- Maximum Offset Voltage: 10μV
- Maximum Offset Voltage Drift: 50nV/°C
- Supply Current: 80µA
- Minimum CMRR: 110dB
- Minimum PSRR: 105dB
- Single Supply Operation: 4.75V to 16V
- Common Mode Range Includes GND
- Output Swings to GND
- Typical Overload Recovery Time: 70ms
- Pin Compatible with Industry Standard Dual Op Amps

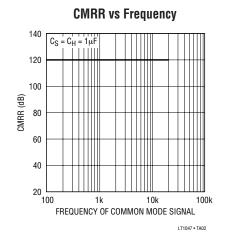
## **APPLICATIONS**

- Thermocouple Amplifiers
- Electronic Scales
- Battery Powered Instrumentation
- Strain Gauge Amplifiers
- Remote Located Sensors

# TYPICAL APPLICATION

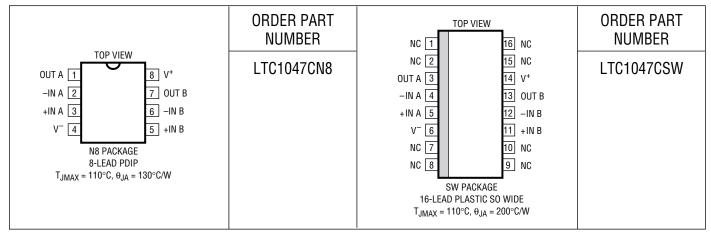
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Micropower Single Supply Instrumentation Amplifier



### ABSOLUTE MAXIMUM RATINGS (Note 1)

### PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

# **ELECTRICAL CHARACTERISTICS** The $\bullet$ denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T<sub>A</sub> = 25°C. V<sub>S</sub> = ±5V, unless otherwise noted.

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Input Offset Voltage	T <sub>A</sub> = 25°C (Note 3)			±3	±10	μV
Average Input Offset Voltage Drift	(Note 3)	•		±0.01	±0.05	μV/°C
Long Term Offset Drift				100		nV√mo
Input Bias Current		•		±5	±30 ±300	pA pA
Input Offset Current		•		±10	±60 ±150	pA pA
Input Noise Voltage	0.1Hz to 10Hz 0.1Hz to 1Hz			3.5 0.8		μV <sub>P-P</sub> μV <sub>P-P</sub>
Input Noise Current	f = 10Hz (Note 4)			1.5		fA√Hz
Common Mode Rejection Ratio	$V_{CM} = V^{-}$ to 2.7V, $T_{A} = 25^{\circ}C$	•	110 105	130		dB dB
Power Supply Rejection Ratio	$V_{\rm S} = \pm 2.375 V \text{ to } \pm 8 V$	•	105	122		dB
Large-Signal Voltage Gain	$R_{L} = 100k, V_{OUT} = \pm 4V$	•	120	150		dB
Maximum Output Voltage Swing	$R_{L} = 10k$ $R_{L} = 100k$	•	4.8/-4.9	4.3/-4.8 ±4.95		V V





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temperature range, otherwise specifications are at  $T_A = 25^{\circ}C$ .  $V_S = \pm 5V$ , unless otherwise noted.

PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNITS
Slew Rate	R <sub>L</sub> = 100k, C <sub>L</sub> = 50pF		0.2		V/µs
Gain Bandwidth Product			200		kHz
Supply Current/Amplifier	No Load, $25^{\circ}C \le T_A \le 70^{\circ}C$ No Load, $0^{\circ}C \le T_A \le 25^{\circ}C$		60 80	150 275	μΑ μΑ
Internal Sampling Frequency			680		Hz

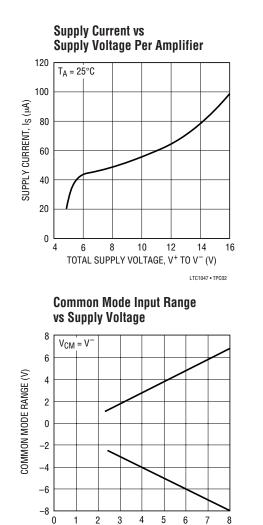
**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:** Connecting any terminal to voltages greater than V<sup>+</sup> or less than V<sup>-</sup> may cause destructive latch-up. It is recommended that no sources operating from external supplies be applied prior to power-up of the LTC1047.

**Note 3:** These parameters are guaranteed by design. Thermocouple effects preclude measurements of these voltage levels in high speed automatic test systems.  $V_{OS}$  is measured to a limit determined by test equipment capability.

**Note 4:** Current Noise is calculated from the formula:  $I_n = \sqrt{2q \cdot I_B}$  where  $q = 1.6 \cdot 10^{-19}$  Coulomb.

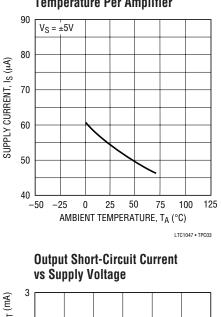
# TYPICAL PERFORMANCE CHARACTERISTICS

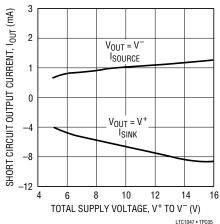


SUPPLY VOLTAGE (±V)

I TC1047 • TPC04

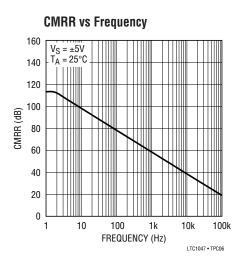
Supply Current vs Temperature Per Amplifier



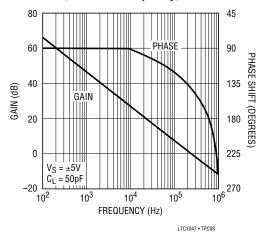


**LINEAR** TECHNOLOGY 1047fa

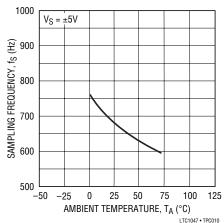
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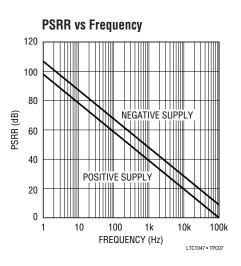




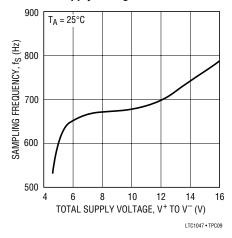


Sampling Frequency vs Temperature

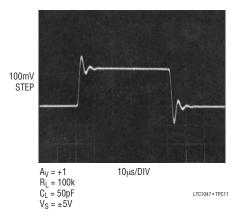




Sampling Frequency vs Supply Voltage

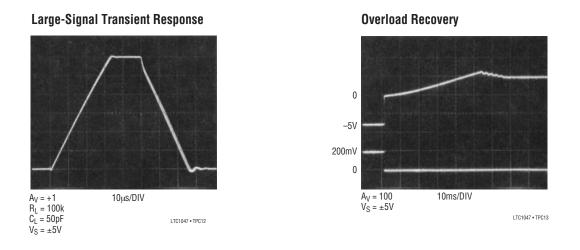


**Small-Signal Transient Response** 



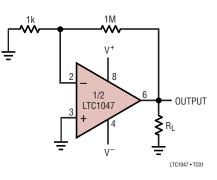


### TYPICAL PERFORMANCE CHARACTERISTICS

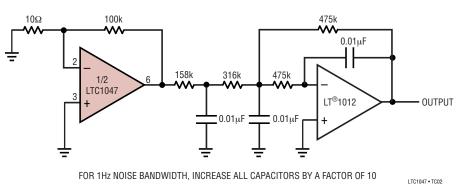


### **TEST CIRCUITS**

#### **Electrical Characteristics Test Circuit**



#### DC to 10Hz Noise Test Circuit



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### APPLICATIONS INFORMATION

### **Input Considerations**

Frequently circuits built with parts as precise as the LTC1047 show errors at the output far greater than the designer expects. Rarely is the problem the op amp; more often the surrounding circuitry is causing errors several orders of magnitude greater than those due to the LTC1047. Such obscure effects as leakage between pins, due to residual solder flux and thermocouple effects between the tin/lead solder and the copper PC board traces, can overwhelm the pA-level bias currents and the  $\mu$ V-level offset of the LTC1047. For a more complete description of these types of problems (and some advice on avoiding them), see the LTC1051/LTC1053 data sheet.

### **Input Capacitance**

The LTC1047 has approximately 12pF of capacitance at each input pin. This will react with large series resistors to form a pole at the input, degrading the LTC1047's phase margin. The problem is especially common with micropower parts like the LTC1047 because high value resistors are often used to minimize power dissipation. As a rule of thumb, bypass feedback resistors larger than 7k with a 20pF capacitor to minimize this effect.

### Aliasing

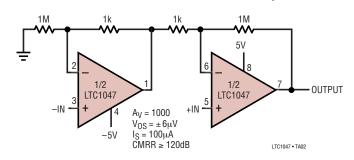
Like all sampled data systems, the LTC1047 will alias input signals near its internal sampling frequency. The design includes internal circuitry to minimize this effect; as a result, most applications do not exhibit aliasing problems. For a complete discussion of the correction circuitry and aliasing behavior, refer to the LTC1051/LTC1053 data sheet.

### **Single Supply Operation**

The LTC1047 is compatible with all single supply applications. It has an input common mode range which includes V<sup>-</sup>, and an output which will swing within millivolts of the negative power supply. The LTC1047 is guaranteed functional down to 4.75V total supply, allowing it to run from minimum TTL voltage all the way up to 16V. See the Typical Applications section for examples of single supply operation.

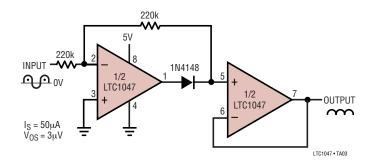


### **TYPICAL APPLICATIONS**

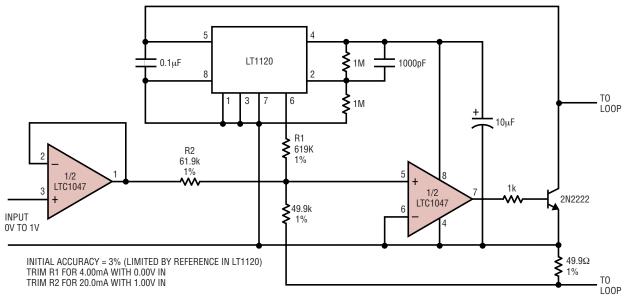


Low Offset, Low Drift Instrumentation Amplifier

Precise DC Full-Wave Rectifier (Absolute Value Circuit)



### TYPICAL APPLICATIONS



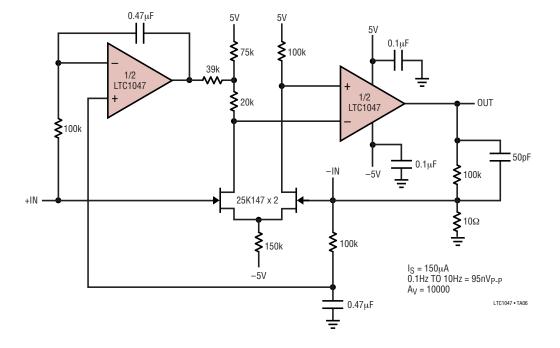
4mA to 20mA Transducer Amplifier

LTC1047 • TA04

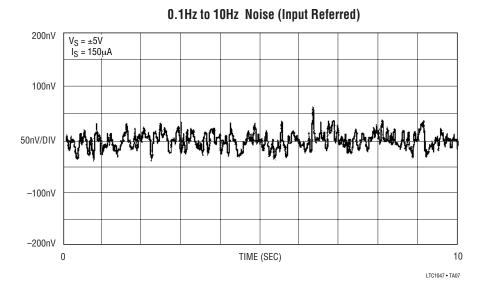


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### **TYPICAL APPLICATIONS**

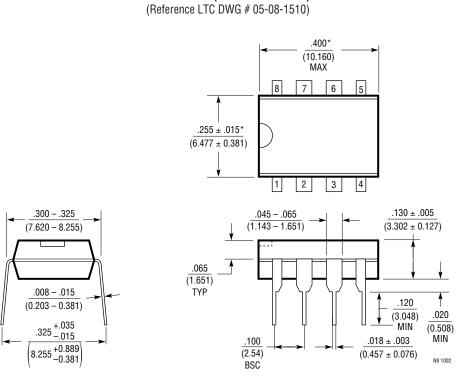


Ultralow Noise Micropower Chopper





### **PACKAGE DESCRIPTION**



N8 Package 8-Lead PDIP (Narrow .300 Inch)

NOTE:

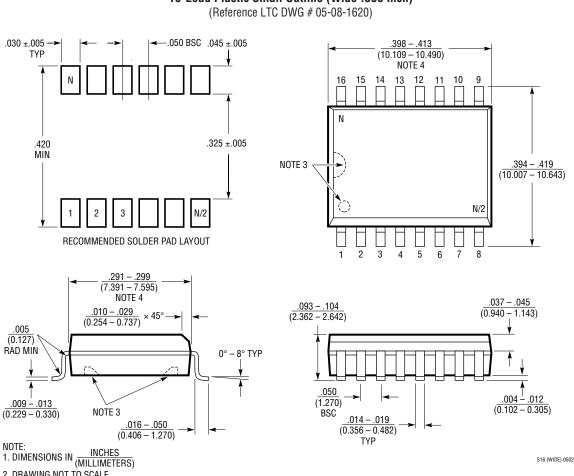
1. DIMENSIONS ARE MILLIMETERS

\*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)



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### PACKAGE DESCRIPTION



SW Package 16-Lead Plastic Small Outline (Wide .300 Inch)

2. DRAWING NOT TO SCALE

3. PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS.

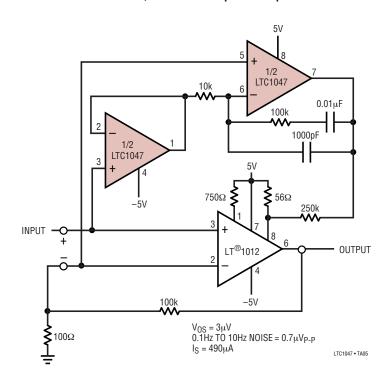
THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

4. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)



### **TYPICAL APPLICATION**



Low Noise, Low Drift Composite Amplifier